L Number	Hits	Search Text	DB	Time stamp
1	38	(soft adj macro) and ((hard adj macro) or (firm adj macro))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 12:59
2	2139	interconnect$3 adj3 information	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:00
3	5274	optimiz$3 adj4 design	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:01
4	274323	optimiz$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:01
5	11000	design$3 adj3 integrated adj3 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:02
6	10911	bandwidth and latency	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:03
7	8611	(design adj3 environment) or (design adj3 development)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:04
8	390	(optimiz$3 adj4 design) and (design$3 adj3 integrated adj3 circuit)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:04
9	107	((design adj3 environment) or (design adj3 development)) and ((optimiz$3 adj4 design) and (design$3 adj3 integrated adj3 circuit))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:04
10	0	(((design adj3 environment) or (design adj3 development)) and ((optimiz$3 adj4 design) and (design$3 adj3 integrated adj3 circuit))) and (bandwidth and latency)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:05
11	127	(interconnect$3 adj3 information) and (bandwidth and latency)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:05
12	3	(optimiz$3 adj4 design) and ((interconnect$3 adj3 information) and (bandwidth and latency))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:07
13	45	(interconnect$3 adj3 information) and (optimiz$3 adj4 design)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:07
14	24	(design$3 adj3 integrated adj3 circuit) and ((interconnect$3 adj3 information) and (optimiz$3 adj4 design))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 13:08

-	5235	(optimiz$3 adj4 integrate adj2 circuit) or (optimiz$3 adj3 design$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:28
-	0	self-programmable adj3 integrated adj2 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:54
-	394	(design$3 adj4 integrated adj circuit) and ((optimiz$3 adj4 integrate adj2 circuit) or (optimiz$3 adj3 design$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:57
-	0	(self?programmable same integrated adj2 circuit) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:57
-	0	((design$3 adj4 integrated adj circuit) and ((optimiz$3 adj4 integrate adj2 circuit) or (optimiz$3 adj3 design$3))) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:58
-	6	(design$3 adj4 integrated adj circuit) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 14:58
-	7	((optimiz$3 adj4 integrate adj2 circuit) or (optimiz$3 adj3 design$3)) and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:04
-	8	self?programmable same integrated adj2 circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:37
-	12467	design$3 adj4 integrated adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:16
-	38	(soft adj macro) and ((hard adj macro) or (firm adj macro))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:33
-	19	optimiz$3 and ((soft adj macro) and ((hard adj macro) or (firm adj macro)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:34
-	0	((design$3 adj4 integrated adj circuit) and ((optimiz$3 adj4 integrate adj2 circuit) or (optimiz$3 adj3 design$3))) and self?programmable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:38
-	2	((optimiz$3 adj4 integrate adj2 circuit) or (optimiz$3 adj3 design$3)) and self?programmable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:38
-	2	(design$3 adj4 integrated adj circuit) and self?programmable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:38

-	68	self?programmable	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/02 15:39
-	2447	design adj3 environment	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:45
-	274323	optimiz$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:30
-	660	(design adj3 environment) and optimiz$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:30
-	172	macro and ((design adj3 environment) and optimiz$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:30
-	111	hard and (macro and ((design adj3 environment) and optimiz$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:30
-	47	soft and (hard and (macro and ((design adj3 environment) and optimiz$3)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:31
-	1080	design adj environment	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:49
-	54	(soft and macro) and ((hard adj macro) or (firm adj macro))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:47
-	2	(design adj environment) and ((soft and macro) and ((hard adj macro) or (firm adj macro)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:47
-	28	(design adj environment) same optimiz$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/06/03 07:49